# Lecture 2: Quantum Circuit

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The standard model of quantum computation is a gate-based model of quantum computation.

- A gate is a unitary transformation acting on O(1) qubits.
- A quantum circuit is a sequence of quantum gates.

In quantum computing literature, there are standard gates and gate identities that are often used. The purpose of this lecture is to explain the motivation behind these standard gate set and the ideas behind how the identities are derived.

# **Circuit Diagram**



Part 1. Universal Gate set

Quantum computation, at the highest level, is a unitary transformation acting on n qubits. (Nielsen and Chang),

#### Fact

Arbitrary unitary  $U \in \mathcal{B}(\mathcal{H}_2^{\mathfrak{A}} . \mathcal{B}_2)$  can be decomposed into a sequence of one- and two-qubit gates.

#### Universal Gate Set

A gate set G is *universal* if any unitary can be approximated arbitrarily well using the set of gates in G.

### Is quantum computing analog?

QC is malog.

The set of unitaries is not a finite set. So it appears that However, surprisingly, any element in this set can be approximated arbitrarily well by a *finite set* of oneand two-qubit gates. It is in this sense quantum computing is "digital."

### Is quantum computing analog?

Some of the early critiques of quantum computing said that it will be impossible to do quantum computation because coherence over exponentially many branches will be very difficult to maintain. We now know that this is a fallacious argument.

In the theory of quantum error correction, one can detect the presence/absence of error by performing a measurement. This measurement process "collapses" the state onto one of <u>discrete</u> set of states, after which the error can be corrected. This is another sense in which quantum computing is "digital."

It is well-known that almost all discrete gate set is universal [Harrow, Recht, Chuang (2001)]. So there is a natural question: which gate set should we choose?

### Experimental Constraint

- Current hardware error rate:  $10^{-2} \sim 10^{-3}$
- Number of gates needed to solve commercially useful problems:  $10^{10} \sim 10^{15}$ .

 $\rightarrow$  Error correction is absolutely necessary to do something useful.

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Quantum Error Correction is compatible only with a rather specific set of gates. All these gates can be cleanly organized into what is known as the Clifford Hierarchy. Part 2. Clifford Hierarchy

- 1. Level-0: Paulis P. P. --
- 2. Level-1: Unitaries that send Paulis to Paulis :  $U = s.\tau$ ,  $UP_1 V^{\dagger}$  is Pauli
- 3. Level-2: Unitaries that send Paulis to Level-1 UP, Ut : level (
- 4. Level-3: Unitaries that send Paulis to Level-2  $UP_{V}^{+}$ : Level 2
- 5. . . .

$$I = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, X = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}, Y = \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix}, Z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}.$$
  
For any Pauli  $P, Q \neq I$ ,  
$$6^{\mathfrak{a}} = \begin{pmatrix} 0 & J \\ c_0 \end{pmatrix} = 6^{\mathfrak{a}} = 6^{\mathfrak{a}}$$

- $P^2 = I$ .
- $\{P, Q\} = 0.$
- XY = iZ, YZ = iX, ZX = iY.YX = -iZ



 $\chi^{2} = I$   $T_{r}(\chi) = 0$   $\chi = \bigcup \begin{pmatrix} \lambda_{1} & 0 \\ 0 & \lambda_{2} \end{pmatrix} \bigcup^{+} \qquad \chi^{2} = \bigcup \begin{pmatrix} \lambda_{1}^{1} & 0 \\ 0 & \lambda_{2}^{1} \end{pmatrix} \bigcup^{+}$  = I  $\begin{pmatrix} \lambda_{1}^{1} & 0 \\ 0 & \lambda_{2}^{1} \end{pmatrix} = I$   $\lambda_{1}, \lambda_{2}$  = I  $T_{r}(\chi) = \lambda_{1} + \lambda_{2}$ 

$$P = \pm P_1 \otimes P_2 \otimes \dots P_{n-1} \otimes P_n$$

$$P_k \in \{J, X, Y, Z\}$$

$$I = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, X = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}, Y = \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix}, Z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}.$$

For any multi-qubit Paulis P, Q

• 
$$P^{2} = I$$
  
•  $[P, Q] = 0 \text{ or } \{P, Q\} = 0.$   
 $(P, Q) = 0 \text{ or } \{P, Q\} = 0.$   
 $(P, Q) = 0 \text{ or } \{P, Q\} = 0.$   
 $(P_{1} \otimes \cdots \otimes P_{n}) (|\phi_{1} \rangle \otimes \cdots \otimes |\phi_{n} \rangle) = P_{1}|\phi_{1} \rangle \otimes \cdots \otimes P_{n}|\psi_{n} \rangle$   
 $(P_{1} \otimes \cdots \otimes P_{n}) (P_{1} \otimes \cdots \otimes P_{n}) = P_{1}|\rho_{1} \otimes \cdots \otimes P_{n}|F_{n}$   
 $= I \otimes \cdots \otimes I.$   
 $(Z \otimes Z) (X \otimes X) = Z X \otimes Z X = (-XZ) \otimes (-XZ) = (-1)^{2} X Z \otimes X^{2}$   
 $= (X \otimes X) (Z \otimes Z)$ 

## **Clifford Unitaries**

### Definition

A *n*-qubit unitary U is Clifford if for all Pauli P,  $UPU^{\dagger}$  is a Pauli.

ex) H, S, CNOT, ...

\*Ixxxxxx UPUte 2#1, ±x, ±x, ±zy

# Single-qubit Clifford Unitaries

$$I = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, X = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}, Y = \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix}, Z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}.$$
$$H = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}, S = \begin{pmatrix} 1 & 0 \\ 0 & i \end{pmatrix}$$
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$$H = \frac{1}{\sqrt{2}} \begin{pmatrix}$$

## **Two-qubit Clifford Unitaries**

$$I = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, X = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}, Y = \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix}, Z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}$$

ex) CX (=CNOT), CZ

 $(1)\otimes 14) = 10\otimes 14)$  $(1)\otimes 14) = 10\otimes 14)$  CU (1008140)= 1008147 CU (1170140)= 1170 U147



 $CX = |0\rangle\langle 0|\otimes I + |1\rangle\langle 0|\otimes X$   $CU = |0\rangle\langle 0|\otimes I + |1\rangle\langle 0|\otimes U$ 

CZ=1076102+ 11751182 = 1016561+ Z0117511









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Clifford unitaries form a group, and this group can be generated by H, S, and CNOT.

### Gottesman-Knill theorem

The exact amplitude/expectation value of any Pauli over a state created by applying a Clifford to  $|0...0\rangle$  can be efficiently computed on a classical computer.

 $\rightarrow$  Clifford unitaries are classically efficiently simulable.

*T***-gate** 

$$T = \begin{pmatrix} 1 & 0 \\ 0 & \sqrt{i} \end{pmatrix}$$

This gate is in the second level of the Clifford hierarchy.

 $TXT^{+} \in UiHard$   $TYT^{+} \in UiHard$   $TYT^{+} \in Ui$   $TzT^{+} \in Ui$   $TXT^{+} = \begin{pmatrix} 1 & 0 \\ 0 & Vx \end{pmatrix} \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix} \begin{pmatrix} 1 & 0 \\ 0 & Vx^{-1} \end{pmatrix}$   $= \begin{pmatrix} 0 & Vx^{-1} \\ Vx & 0 \end{pmatrix}$   $= \sqrt{1} \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}$ 

$$SX = \begin{pmatrix} I \circ \\ \circ & \lambda \end{pmatrix} \begin{pmatrix} \circ & I \\ I \circ & 0 \end{pmatrix}$$
$$= \begin{pmatrix} 0 & 1 \\ \lambda & \circ \end{pmatrix}$$

## Toffoli gate

Toffoli gate is also in the second level of the Clifford hierarchy.



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Tallal 11 CCX /1>11>14> = (1>11> X)4> CCX (21>12>14> = (2>13>14> Otherwise Ci.e, 2+1 or 5+1)

CCK (X ØIØI) (CX /0>8/4/8/4/> CCK (X ØIØI) (CX /1>8/4/8/4/>



The following gate sets are universal.

- Clifford + T
- Clifford + Toffoli

These are often the standard gate sets that people use in the literature.

Part 2. Side comment: Algorithm to gates

Often a quantum algorithm is specified in terms of sequence of *continuous gates.* How can this be translated into a sequence of discrete gate set?

### Solovay-Kitaev theorem

**Theorem.** Let  $\mathcal{G}$  be a finite set of elements in SU(2) containing their inverses such that the group they generate is dense in SU(2). For any  $\epsilon > 0$ , there is a constant  $\underline{c}$  such that for any  $U \in SU(2)$ , there is a sequence of gates in  $\mathcal{G}$ (denoted as S) such that

$$||S - U|| \le \epsilon. = O(\log \frac{1}{\epsilon})$$

\* Remark: The proof is constructive.

The Solovay-Kitaev theorem is already pretty good, but for Clifford+T, we can do much better. We can approximate any gate in SU(2) up to an error  $\epsilon$  using  $O(\log 1/\epsilon)$  gates. [Kliuchnikov and Mosca, Ross and Selinger]

ε	T-count	T-bound	Actual error	Runtime	Candidates	Time/Candidate
$10^{-10}$	102	$\geqslant 102$	$0.91180 \cdot 10^{-10}$	0.0190s	3.0	0.0064s
$10^{-20}$	200	$\geq 198$	$0.87670 \cdot 10^{-20}$	0.0433s	7.0	0.0061s
$10^{-30}$	298	$\geqslant 298$	$0.99836 \cdot 10^{-30}$	0.0600s	7.0	0.0085s
$10^{-40}$	402	$\geq 400$	$0.77378 \cdot 10^{-40}$	0.0976s	11.7	0.0084s
$10^{-50}$	500	$\geq 500$	$0.82008 \cdot 10^{-50}$	0.1353s	20.3	0.0067s
$10^{-60}$	602	$\geqslant 596$	$0.61151\cdot 10^{-60}$	0.1548s	16.0	0.0097s
$10^{-70}$	702	$\geq 698$	$0.40936 \cdot 10^{-70}$	0.1931s	20.9	0.0093s
$10^{-80}$	804	$\geqslant 794$	$0.92372 \cdot 10^{-80}$	0.2402s	27.2	0.0088s
$10^{-90}$	898	$\geq 898$	$0.96607 \cdot 10^{-90}$	0.2696s	22.2	0.0121s
$10^{-100}$	1000	$\geq 998$	$0.78879 \cdot 10^{-100}$	0.3443s	31.2	0.0110s
$10^{-200}$	1998	$\geq 1994$	$0.73266 \cdot 10^{-200}$	1.1423s	62.3	0.0183s
$10^{-500}$	4990	$\geqslant 4986$	$0.67156\cdot 10^{-500}$	8.6509s	170.4	0.0508s
$10^{-1000}$	9974	$\geqslant 9966$	$0.80457 \cdot 10^{-1000}$	47.9300s	270.4	0.1773s
$10^{-2000}$	19942	$\geq 19934$	$0.88272 \cdot 10^{-2000}$	383.1024s	556.7	0.6881s

From Ross and Selinger (2014).



Another evidence that quantum computing is digital!

Part 3. Gate/Circuit Identities

When we convert an algorithm into one- and two-qubit gates, there are many well-known identities that people use.

The gate identities involving a single qubit is often straightforward and easy to work out via brute-force calculation. However, multi-qubit identities tend to be trickier. We will discuss several tricks.

Suppose we are given a unitary U described in terms of a sequence of gates. How can we implement the following?

 $(lpha|0
angle\otimes+eta|1
angle)|\psi
angle
ightarrowlpha|0
angle\otimes|\psi
angle+eta|1
angle\otimes U|\psi
angle$ 



## Multiplying controlled unitaries

We know that

$$ZX|\psi\rangle = iY|\psi\rangle \equiv Y|\psi\rangle,$$

because the global state does not matter in quantum mechanics.

What about  $CZ \cdot CX$ ?







It will become pretty evident in the next lecture that the gates of the form of  $e^{i\frac{P}{P}}$ , where P is a multi-qubit Pauli, appears very frequently in quantum algorithms. How can we decompose this into one-and two-qubit gates?



# Decomposing Toffoli

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How do you decompose a Toffoli into one- and two-qubit gates?



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$$\begin{split} \chi = H Z H \\ & (Z = e^{\lambda Z_{1}} \left( \frac{1 - Z_{1}}{2} \otimes \frac{1 - Z_{2}}{2} \otimes \frac{1 - Z_{3}}{2} \right) \qquad Z = \begin{pmatrix} 0 & 0 \\ 0 & -1 \end{pmatrix} \\ & I - Z = \begin{pmatrix} 0 & 0 \\ 0 & -1 \end{pmatrix} \\ & I - Z = \begin{pmatrix} 0 & 0 \\ 0 & 1 \end{pmatrix} \\ e^{\lambda Z_{1}} \left( \frac{1}{8} (I - Z_{1} - Z_{1} - Z_{1} + Z_{1} Z_{1} + Z_{1} Z_{1} - Z_{1} - Z_{1} - Z_{1} Z_{1} - Z_{1} - Z_{1} - Z_{1} Z_{1} - Z_{1} - Z_{1} Z_{1} - Z_{1} - Z_{1} - Z_{1} Z_{1} - Z_{1}$$

- 1. Universal Gate Set: It makes a lot of sense to use Clifford + T
- 2. Algorithm  $\rightarrow$  one-and two-qubit gates  $\rightarrow$  Clifford + T
- 3. Various circuit identities: These will prove to be useful for tomorrow's lecture.